ABSTRACT OF THE DISCLOSURE

A game apparatus includes a CPU, and a ROM and a RAM which are integrated in a cartridge detachably loaded into the game apparatus. The ROM stores a plurality of game programs, and the RAM has backup data storing areas for the respective game programs and a shared backup data storing area. The shared backup data area is written with shared information utilized in common by a plurality of games. When a predetermined condition is accomplished at a result of progress of a game instructed to be started out of the plurality of games, the CPU writes information relating to the predetermined condition to a backup area of the started game and also writes the same to a backup area of another game.